

Electronic Acknowledgement Receipt

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Application Number:	10719020
International Application Number:	
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Title of Invention:	METHOD FOR PREPARING A CIRCUIT BOARD MATERIAL HAVING A CONDUCTIVE BASE AND A RESISTANCE LAYER
First Named Inventor/Applicant Name:	Akira Matsuda
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)

1	Issue Fee Payment (PTO-85B)	032130_Issue_Fee_Transmittal _08_10_2010.pdf	91764 b789d6fcdf436d062511lbcbl42b916501 14e49	no	1
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	31932 ae175f82e49e9a1640d11aa4edf0c80e0f52 1e45	no	2
Warnings:					
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Total Files Size (in bytes):				123696	
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